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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Papa Rao et al. Art Unit: 2818
Serial No.: 10/697,139 Examiner: Dang, P.
Filing Date: 10/30/2003 Docket No.: TI-36382
Customer No.: 23494 Conf. No.: 9759
Title: CAPACITOR INTEGRATION AT TOP-METAL LEVEL WITH A
PROTECTION LAYER FOR THE COPPER SURFACE

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by
facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on
the date shown below:


Jacqueline J. Garner, Reg. No. 36,144

August 1, 2005
Date

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner
for an extension of the shortened statutory period for response in the above-identified
Application.

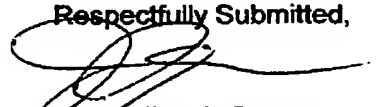
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,


Jacqueline J. Garner
Reg. No. 36,144

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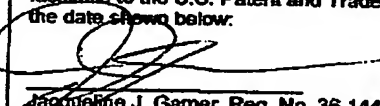
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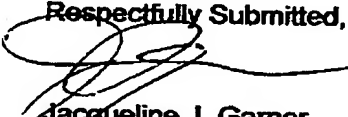
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